

THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Matthew J. Adiletta, et al.      Art Unit: 2187  
Serial No.: 09/387,109      Examiner: D. Diller  
Filed : August 31, 1999  
Assignee : Intel Corporation  
Title : SDRAM CONTROLLER FOR PARALLEL PROCESSOR ARCHITECTURE

**Mail Stop Amendment**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicants call attention to the attached Information Disclosure Statement and documents listed on form PTO-1449.

Copies of the U.S. patents/publications are not being provided per the waiver posted on the PTO website.

Kindly accept this Information Disclosure Statement under Rule 97(c)(2). The rule 17(p) certification fee of \$180 is enclosed.

The documents are in the English language; hence no concise explanation is necessary per Rule 98(a)(3).

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CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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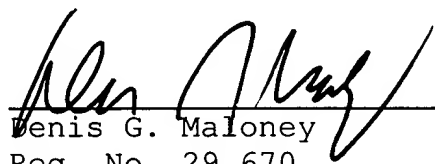
Attorney's Docket No.:10559-079001

Consideration of the foregoing and enclosures plus the return of a copy of the enclosed form PTO-1449 with the Examiner's initials in the left column per MPEP 609 are earnestly solicited along with an early action on the merits.

Please apply any additional charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

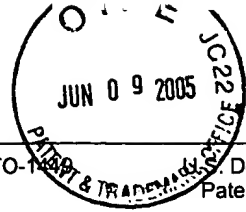
Date: 6/7/05

  
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Denis G. Maloney

Reg. No. 29,670

Attorney for Intel Corporation

Fish & Richardson P.C.  
USPTO Customer No. **20985**  
12390 El Camino Real  
San Diego, CA 92130  
Telephone: (858) 678-5070  
Facsimile: (858) 678-5099



<b>Substitute Form PTO-1449</b> (Modified)  <b>Information Disclosure Statement by Applicant</b> (Use several sheets if necessary)  (37 CFR §1.98(b))	Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-079001	Application No. 09/387,109
	Applicant Matthew J. Adiletta, et al.		
	Filing Date August 31, 1999	Group Art Unit 2187	

U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	4,016,548	04/1977	Law, et al.			
	AB	4,032,899	06/1977	Jenny, et al.			
	AC	4,075,691	02/1978	Davis, et al.			
	AD	4,831,358	05/1989	Ferrio, et al.			
	AE	5,568,476	10/1996	Sherer, et al.			
	AF	5,764,915	06/1998	Heimsoth, et al.			
	AG	5,850,530	12/1998	Chen, et al.			
	AH	5,918,235	06/1999	Kirshenbaum, et al.			
	AI	5,995,513	11/1999	Harrand, et al.			
	AJ	6,061,710	05/2000	Eickemeyer, et al.			
	AK	6,085,215	07/2000	Ramakrishnan, et al.			
	AL	6,111,886	08/2000	Stewart			
	AM	6,170,051	01/2001	Dowling			
	AN	6,212,542	04/2001	Kahle, et al.			
	AO	6,223,274	04/2001	Catthoor, et al.			
	AP	6,338,078	01/2002	Chang, et al.			
	AQ	6,366,998	04/2002	Mohamed			
	AR	6,453,404	09/2002	Bereznai, et al.			
	AS	6,463,072	10/2002	Wolrich, et al.			
	AT	6,463,480	10/2002	Kikuchi, et al.			
	AU	6,463,527	10/2002	Vishkin			
	AV	6,466,898	10/2002	Chan			
	AW	6,529,983	03/2003	Marshall, et al.			
	AX	6,535,878	03/2003	Guedalia, et al.			
	AY	6,584,522	06/2003	Wolrich, et al.			
	AZ	6,587,906	07/2003	Wolrich, et al.			
	AAA	6,604,125	08/2003	Belkin			

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

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U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	ABB	6,606,704	08/2003	Adiletta, et al.			
	ACC	6,625,654	09/2003	Wolrich, et al.			
	ADD	6,661,794	12/2003	Wolrich, et al.			
	AEE	6,668,317	12/2003	Bernstein, et al.			
	AFF	6,671,827	12/2003	Guilford, et al.			
	AGG	6,792,488	09/2004	Wolrich, et al.			
	AHH						

Foreign Patent Documents or Published Foreign Patent Applications								
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	AII							
	AJJ							

Other Documents (include Author, Title, Date, and Place of Publication)		
Examiner Initial	Desig. ID	Document
	AKK	"10-/100-Mbps Ethernet Media Access Controller (MAC) Core", NEC, 1998, pages 1-5.
	ALL	"Nomadic Threads: A migrating multithread approach to remote memory accesses in multiprocessors", by Jenks, S.; Gaudiot, J.L. (abstract only) Publication Date: 20-23 Oct. 1996.
	AMM	"Overview of the START (*T) multithreaded computer" by Beckerie, M.J. (abstract only) Publication Date: 22-26 Feb. 1993.
	ANN	A. Ippoliti, et al., "Parallel Media Access Controller for Packet Communications at Gb/s Rates", 1990, IEEE, pages 991-996.
	AOO	Howard Frazier, "Gigabit Ethernet: From 100 to 1,000 Mbps", 1999, IEEE Internet Computing, pages 24-31.
	APP	Howard Frazier, "The 802.3z Gigabit Ethernet Standard", 1998, IEEE Network, pages 6-7.
	AQQ	

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